エンボステープパッケージングにおける電子デバイスの帯電問題

藤村 徹夫*,清水 美基雄*,鍋田 健司*,川田 正寿**,岡本 彰夫**

Electrification Subject of the Electron Device in Emboss Tape Packaging

Tetsuo FUJIMURA*, Mikio SHIMIZU*, Takeshi NABETA*, Masatoshi KAWATA** and Akio OKAMOTO**

- *電気化学工業株式会社加工技術研究所(〒372-0855 群馬県伊勢崎市長沼町西河原245)
- **電気化学工業株式会社中央研究所基盤技術研究室(〒194-8560 東京都町田市旭町3-5-1)
- *Polymer Processing Technology R&D Center, Denki Kagaku Kogyo Kabushiki Kaisya (245 Nishigawara, Naganuma-cho, Isesaki-shi, Gunma 372-0855)
- **Basic Research Laboratory, Research Center, Denki Kagaku Kogyo Kabushiki Kaisya (3-5-1 Asahimachi, Machida-shi, Tokyo 194-8560)

Abstract

Recent electric devices, such as LEDs and chip resistances are remarkably decreasing their sizes. Such small devices may be attached to packed cover tape, however, when the tape is removed in an assembly process. This problem often remains even if cover tape is treated with antistatic agents or electroconductive materials because these devices are also charged by contact or friction with cover tape. Alternative packaging materials are required to suppress electrostatic force generated with electric devices.

Key Words: Electron Device, Adhesion, Charge, Suppress Electrification