Formation of Au Microbump Arrays for Flip-Chip Bonding Using Electroless AuDeposition from a Non-Cyanide Plating Bath


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(Received August 11, 2009; accepted November 2, 2009)

Abstract

The formation of Au microbump arrays for flip-chip bonding was investigated using an electroless Au plating bath. Generally, electroless Au deposition gives a low deposition rate, preventing the fabrication of Au bumps with heights of more than 5 μm. To overcome this problem, we developed a new electroless Au deposition process that employs a non-cyanide bath. This process delivers a high deposition rate, especially for fine bump patterns (bumps of less than 10 μm in diameter). Au bumps with a diameter and height of 10 μm each could be fabricated with a high deposition rate using the new electroless Au plating bath. Moreover, Au microbump arrays were successfully fabricated on Cu wiring. The practical feasibility of this technique was demonstrated by forming arrays with bump heights and bump diameters of 10 μm each and an array pitch of 30 μm. All film-formation processes were performed under cyanide-free conditions; thus, this process has a high industrial potential for fabricating ultra-fine Au microbump arrays.

Keywords: Electroless Au Deposition, Non-Cyanide Bath, Flip-Chip Bonding, Bump

1. Introduction

Interconnect technologies, such as chip-to-chip and chip-to-substrate, are very important for fabricating high-performance electrical devices.[1] To achieve the required performance, high-density interconnects are necessary, and flip-chip bonding is one of the best methods for achieving such interconnects. In recent years, interconnect density has increased and bump sizes for first-level interconnects have decreased to less than 10 μm. Usually, microbumps and solder-bumps are used for flip-chip bonding of chip-to-substrate interconnects.[2–4] When using solder-bumps for interconnects, the bump shape is affected by the reflow process, and decreasing the bump pitch is not easy. On the other hand, it is easy to attain a high-density pad pitch of microbumps, though obtaining bump arrays with a uniform bump height is essential. Au bumps are usually fabricated by electrodeposition. However, due to current distribution, it is not easy to fabricate homogeneous Au bumps with fine pitch (e.g., < 20 μm). Moreover, lead wires supplying the current for Au electrodeposition disturb high-speed signal transmissions.[5]

In this situation, electroless Au deposition is one of the best methods for obtaining fine-pitch Au bumps. The electroless deposition method requires no electrical lead wiring and is not affected by the current distribution. However, conventional electroless Au deposition with high deposition rate is currently feasible only if a cyanide bath is used.[6] With this technique, it is difficult to obtain fine structures because the photosensitive pattern and/or the substrate are damaged by cyan ions during the deposition. On the other hand, the deposition rate of a non-cyanide bath is too low to fabricate Au bumps with heights of more than 5 μm.[6] In addition, a cyanide bath is not desirable from an environmental viewpoint.

In this study, Au microbump arrays with a bump diameter and bump height of 10 μm each (aspect ratio = 1) were fabricated using a newly developed non-cyanide bath. Flip-chip bonding experiments were carried out and some bonding parameters were investigated. Adhesive/barrier layers were developed using an electroless deposition process, and the development and characterization of the Au bump arrays on test chips with wiring was also investigated.
2. Experimental

Low temperature, neutral pH, and cyanide-free conditions are needed for micro-bump formation, because the resist pattern is easily damaged by high temperature, alkaline pH, and/or cyanide. In this investigation, the bath system was based on the electroless Au plating bath “SELFGOLD OTK” (Kanto Chemical Inc.) because this bath is a non-cyanide Au plating bath. Gold(I) trisodium disulphite was used as the metal source with a standard Au concentration was of 2 g dm–3. Mixed ligand system was used for Au-complex and both sulfite and thiosulfate ions were used as complexing agent. Some surfactants and stabilizers were used to improve the stability of the plating bath. The bath pH was 7.2, and the bath temperature was 60°C. However, using this commercial bath, micro-bumps could not be fabricated because the resist patterns were damaged during immersion in the plating bath. A new plating bath is needed based on this commercial bath. For basic investigations, such as those on deposition behavior and die share test, an evaporated Au (100 nm)/Ti (20 nm)/Si wafer (3 in.) was used as the substrate. For advanced investigations, such as those on the formation of barrier layers and the electrical continuity test, an evaporated Cu (100 nm)/Ti (10 nm)/Si wafer (3 in.) was used as the substrate. A resist pattern with a diameter of 5–20 μm, pitch of 10–30 μm, and thickness of about 12 μm was made using photolithography with photoresist (PMER P-LA900PM). Cu wires with a thickness of 3 μm were fabricated by a semi-additive process using Cu electrodeposition, chemical etching, and reactive ion etching. The barrier layers used for Ti evaporation and electroless NiB deposition were fabricated using the lift-off process and the semi-additive method, respectively. Details of the wiring and the barrier layer are discussed below in the Results and Discussion section. The test chip and the substrate were cut to dimensions of 10 × 10 mm² and 15 × 15 mm², respectively. Flip-chip bonding experiments employed a high-precision flip-chip bonder. Bonding tests were carried out using a die share experiment. The shape of the bumps was observed using an optical microscope, laser microscope, and scanning electron microscope (SEM).

3. Results and Discussion

3.1 Electroless Au plating bath with high deposition rate

In general, the rate of electroless Au deposition using a non-cyanide bath is lower than that using a cyanide bath.[6] Figure 1 shows the deposition rate of some electroless Au deposition techniques. To form bump arrays with heights of more than 5 μm, a deposition rate of 3.5 μm/h is necessary to avoid damaging the photoresist during deposition given a bath with a pH of about 7 and a temperature of 50°C. Conventional techniques to increase the deposition rate consist of increasing the bath pH and/or the bath temperature. However, this results in greater damage to the photoresist pattern. Another technique consists of increasing the Au ion and/or the reducing agent concentration, but this technique leads to a decrease in stability. The addition of chemical agents such as an accelerator is very effective in increasing the deposition rate, but

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<th>Bath system</th>
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Fig. 1  Deposition rate of several electroless Au deposition baths on patterned and non-patterned substrates. The filled symbols indicate the results when using optimized conditions.
also leads to modifications in film properties. Thus, we investigated fabricating Au microbumps using a non-cyanide plating bath with a high deposition rate.

First, the acceleration of the cathodic reaction was investigated. The deposition rate of electroless plating is strongly related to the rate-determining step. The deposition rate of the basic plating bath, “SELFGOLD OTK,” is related to the Au ion concentration, indicating that the rate-determining step is the mass transfer of Au ions. Thus, the metal ion concentration was increased from 2 g dm$^{-3}$ to 4 g dm$^{-3}$. The plating bath was stabilized by decreasing the bath temperature from 60°C to 50°C. Moreover, a high concentration of Au ions near the deposition surface was obtained to decrease the deposition area, leading to the nonlinear diffusion of Au ions. A small-diameter bump array pattern is expected to increase the deposition rate. The data of Fig. 1 show that the deposition rate increases with a decrease in the bump diameter (see Fig. 1; row title: electroless Au plating bath K). Using a pattern with a bump diameter of 10 μm, we obtained a deposition rate of 3.5 μm/h. Decreasing the diameter to 5 μm increased the deposition rate to 5 μm/h. Thus, this method is very effective for increasing the deposition rate.

From these results, both a high deposition rate and bath stability were realized with a high Au concentration of 4 g dm$^{-3}$ and low bath temperature of 50°C. The low bath temperature was also utilized to avoid damaging the photore sist during deposition. Hereafter, this bath, having a high deposition rate, is referred to as the “electroless Au plating bath K” (bath K).

Figure 2 shows the Vickers hardness of the deposited non-patterned films. In this research, we used both the SELFGOLD OTK bath and Bath K, and an annealing temperature of 250°C. The film thickness was about 10 μm. The results showed a good Vickers hardness of less than 80 Hv both with and without annealing, which is the same value obtained from the films fabricated using the SELF-GOLD OTK bath. This indicates that the Au bumps fabricated by electroless deposition are suitable for forming fine bump arrays. However, the deposition rate was easily modified by agitation or by using different deposition conditions. Improving the reproducibility of this method, however, is necessary before this plating bath can be used for practical applications.

Next, some additives were introduced into Bath K in order to increase the deposition rate. The concentrations of surfactants and stabilizers were optimized for use with the accelerator. As seen in Fig. 1, the deposition rate increased with a decrease in the bump diameter (see Fig. 1; row title: Electroless Au plating Bath XG). The filled symbols in Fig. 1 indicate the results when using optimized conditions (note, especially, that the deposition rate for the 10-μm-diameter bumps increases). In addition, the stability of the plating bath is excellent, so that no deposition occurs on a non-patterned section of the substrate under optimized accelerator concentration conditions. Thus, this bath series proves very effective for fabricating fine bump arrays with bump diameters of 10 μm. In spite of the high Au concentration with this plating solution, the plating bath showed excellent stability. So it is likely to have a bath-life as long as that of a conventional, non-cyanide bath for electroless Au plating. The volume of Au deposits in the arrays was decreased because of decreasing both the diameter and the height of the bump. This result indicates decreased costs for micro-bump formation. From these results, both high deposition rate and high bath stability were realized by using a high Au concentration, low bath temperature, and additives. Hereafter, this bath, with the optimized accelerator concentration, will be referred to as the “Electroless Au-Plating Bath XG.” (Bath XG).

3.2 Fabrication of Au bumps with a high aspect ratio

We developed a process to fabricate Au bump arrays with bump diameters of 10 μm, bump heights of 10 μm, and an array pitch of 30 μm using Bath XG. In a peripheral array, the number of bumps is about 1100. Figure 3 shows SEM images and a cross-sectional profile of a Au bump array fabricated by electroless deposition. These images show that bumps with a high aspect ratio ($\approx 1$) are successfully formed when using a fine photoresist pattern.
Observation of deposition failure was checked by optical microscope, although a no-deposition failure was not apparent.

Furthermore, we also fabricated bump arrays with bump diameters of 5 μm, 10 μm, and 20 μm, as shown in Fig. 4. The bump heights were controlled by adjusting the deposition time. The figure shows the fabrication of smooth-walled bumps with diameters of 5–20 μm. In particular, 5-μm-diameter bumps had a high aspect ratio of about 2, and with no defects. The deposition reaction of this electroless Au plating bath did not generate hydrogen bubbles, so Au bumps with no defects could be easily obtained without highly controlled agitation systems.

3.3 Fabrication of Au bumps in fine-pitch arrays

Au bump arrays with bump:space = 1:1 were fabricated to realize a finer bump pitch. In this research, Bath K was used, with a fixed deposition time of 90 min. Figure 5 shows SEM images of bump arrays with fine pitches of 20 μm and 10 μm. The bump arrays with diameters of 5 μm and 10 μm have pitches of 10 μm and 20 μm and heights of 5 μm and 7 μm, respectively. We analyzed the bump height deviations before dicing the chip and found deviations for the 5- and 10-μm-diameter bumps to be ±0.2 μm and ±0.1 μm from average height, respectively. We conclude from these results that electroless Au deposition is suitable for fabricating high-density bump arrays with low height deviations.

3.4 Bondability of fine Au bump arrays

To investigate the bondability of the Au bump arrays, we fabricated chips from the Au bump arrays with a bump diameter of 10 μm, bump height of 5 μm, and array pitch of 20 μm. A chip size of 5 mm × 5 mm was used. The chip was mounted on a Au (1.5–2.0 μm)/Au (100 nm)/Ti (20 nm)/Si substrate. Non-patterned Au films with thicknesses of 1.5–2.0 μm were prepared from Bath K by electroless Au deposition. Before flip-chip bonding, the surface of the substrate and the Au bumps on the chips were cleaned by O₂ plasma ashing.
Figure 6 shows cross-sectional SEM images of the chip on the substrate after flip-chip bonding. As the images indicate, the Au bumps on the chip and the Au layer on the substrate were successfully bonded, and no void or seam was observed between the bumps and the Au layer.Die-shear tests revealed a die-shear strength of more than 20 kgf, indicating that a sufficient bonding strength is achieved for this sample.

Flip-chip bonding was also carried out using test chips made from Au bump arrays with a bump diameter of 10 μm, bump height of 10 μm, and array pitch of 30 μm. The chip size for these tests was 10 mm × 10 mm. Other conditions, such as pretreatment, were the same as those used in the above-mentioned investigations. Die-shear tests revealed a die-shear strength of more than 50 kgf, indicating that sufficient bonding strength is achieved.

Fig. 5 SEM images and cross-sectional profiles of Au bump arrays with pitches of (a) 20 μm and (b) 10 μm fabricated by electroless deposition.

Fig. 6 Cross-sectional SEM images of the test chip on the substrate after flip-chip bonding. The bump diameter and array pitch are 10 μm and 20 μm, respectively.

Fig. 7 Process flow for fabrication of a microbump array with wiring.
From these results, it appears that Au bump arrays fabricated with this method are suitable for fine-pitch, flip-chip bonding. Thus, the formation process using a non-cyanide plating bath proves practical for fabricating high-density interconnections consisting of ultra-fine pitch bump arrays.

3.5 Fabrication of Au bumps on fine Cu wiring

Figure 7 shows the process flow for fabricating Au microbump arrays with wiring. An evaporated Cu (100 nm)/Ti (10 nm)/Si wafer was used as the substrate. First, the photoresist pattern for the wiring was prepared, with the thickness of the photoresist being 5 μm. Second, the Cu wiring, with a width of 22 μm and thickness of 3 μm, was prepared by Cu electrodeposition. Third, a Au (50 nm)/Ti (50 nm) layer was prepared by evaporation. The Au layer acts as a seed layer and the Ti layer acts as a barrier layer. Fourth, excess deposits of the barrier film were removed by the lift-off process using acetone. Fifth, the photoresist pattern for the bump array was formed, with the resist thickness and bump diameter being 12 μm and 10 μm, respectively. Sixth, a Au bump array with a bump height of 10 μm was prepared by electroless Au deposition. The final resist was removed, along with excess deposits of the seed layer/barrier layer. The Cu seed layer and Ti barrier layer were removed by chemical etching and reactive ion etching, respectively.

Figure 8 (a) and (b) show the Au micro bump array on a test chip. The array pitch is 30 μm. A smooth-walled Au bump array on Cu wiring was observed. Figure 8 (c) and (d) show Au micropads on the chip substrate. The test substrate was fabricated using the same process flow given in Fig. 7. A Au pad with dimensions of $20 \times 20 \ \mu m^2$ and a thickness of 2 μm was prepared for the bump connection by electroless Au deposition using Bath K.

Using Au bump arrays fabricated by the methods discussed above, we performed chip-to-substrate flip-chip bonding, with the result shown in Fig. 9. The bonding strength is not as high as that in the previous investigation, but the electrical continuity test indicates that good electrical contact is established.

3.6 Fabrication of barrier/adhesive layer by electroless deposition

In the third step given in Fig. 7, the barrier layer was fab-
fabricated using a dry fabrication process. However, for wiring, all-wet fabrication processes were required. To overcome this, we developed an electroless deposition method for the barrier/adhesive layer.

Figure 10 shows the test chip made from Au bump arrays with a bump diameter of 10 μm, bump height of 5 μm, and array pitch of 30 μm. The barrier layer was prepared by the semi-additive method using electroless NiB deposition, and films with thicknesses of 500 nm were produced. Using a NiB bath, we achieved selective deposition without activations (such as Pd activation). Before depositing Au bumps via electroless Au deposition, a Au seed layer was developed using displacement Au deposition. In this research, we used a non-cyanide Au plating bath. These results demonstrate the successful fabrication of Au bump arrays on Cu wiring using a non-cyanide process, and this process has high potential for achieving high-density, flip-chip bonding.

4. Conclusion

We investigated the fabrication of Au microbump arrays deposited by non-cyanide electroless Au plating bath for flip-chip bonding. We developed an electroless Au deposition process to produce fine bump patterns with a high deposition rate using a non-cyanide bath. Using this new bath, we could fabricate Au bumps with a bump diameter of 10 μm and bump height of 10 μm. Flip-chip bonding of the Au bump arrays thus produced resulted in good bonding strengths. Furthermore, we successfully deposited Au microbump arrays on Cu wiring. The practical feasibility of this technique was demonstrated by forming arrays with a bump height of 10 μm, bump diameter of 10 μm, and array pitch of 30 μm. The entire film formation process used a non-cyanide bath; thus, this process shows high potential for fabricating ultra-fine Au bump arrays. High density interconnects are a key requirement for Next-Generation Multi-Functional High-Density 3D Integration Technology.[7] This fabrication method is a candidate interconnect technology for the new 3D integration technology.

References